REMARKS

At the outset, the Examiner is thanked for the thorough review and consideration of the pending application. The Office Action dated October 18, 2005, has been received and its contents carefully reviewed.

Claims 1, 6, 11, 15, 19 and 20 are rejected and claims 2-5, 12-14 and 16-18 are objected to by the Examiner. Claims 1-20 remain pending in this application. Applicant wishes to thank the Examiner for the indication that claim 2-5, 12-14 and 16-18 contain allowable subject matter.

In the Office Action, claims 1, 6-11, 15, 19 and 20 are rejected under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent No. 6,882,376 to Kim et al. (hereinafter "Kim").

The rejection of claims 1, 6, and 11 is respectfully traversed and reconsideration is requested. Claims 1 and 6 are allowable over the cited references in that each of these claims recites a combination of elements including, for example, "a gate bus line in which a bump-shaped groove is formed at a region where the gate bus line crosses and overlaps the data bus line to prevent the data bus line from opening and through which a gate signal is applied." Claim 11 is allowable over the cited references in that this claim recites a combination of elements including, for example, "a gate bus line perpendicularly crossing the data bus line to define a unit pixel region and the gate bus line having an area with a bump structure where the gate bus line overlaps the data bus line." Kim does not teach or suggest at least this feature of the claimed invention.

Examples of the bump-shaped groove or bump structure of the claims is shown in Figures 5, 6, and 7. In each of these examples there is a portion of one side the gate bus line having a recessed area (see 28 and 48) with a bump-shaped groove or bump structure overlapping the data bus line. The bump-shaped groove or bump structure causes a variation in the width of the recessed area where it overlaps the data bus line. In Kim, the recessed portion of the gate bus line does not have the bump-shaped groove or bump structure. The portion of the recess overlapping the data bus line has a constant width that indicates the lack of the bump-shaped groove or bump structure. Accordingly, Applicant respectfully submits that claims 1, 6, and 11 are allowable over Kim.

The rejection of claim 15 is respectfully traversed and reconsideration is requested. Claim 15 is allowable over the cited references in that this claim recites a combination of elements including, for example, "a gate bus line perpendicularly crossing the data bus line to define a unit pixel region and being shaped in a saw tooth structure at edge portion of an area where the gate bus line overlaps the data bus line to prevent the data bus line from opening." Kim does not teach or suggest at least this feature of the claimed invention. Clearly Kim lacks any kind of saw tooth structure at an edge portion of the gate bus line. Accordingly, claim 15 is allowable over Kim.

The rejection of claims 19 and 20 is respectfully traversed and reconsideration is requested. Claims 19 and 20 are allowable over the cited references in that each of these claims recites a combination of elements including, for example, "a second bus line formed before the first bus lines are formed, and having an edge overlapped with the first bus line and shaped in a non-linear structure so that the first bus line is prevented from opening." Kim does not teach or suggest at least this feature of the claimed invention.

Examples of the non-linear structure of the claim is shown in Figures 5, 6, and 7. In each of these examples there is a portion of one side the gate bus line having a recessed area (see 28 and 48) with a non-linear structure overlapping the data bus line. The non-linear structure causes a variation in the width of the recessed area where it overlaps the data bus line. In Kim, the recessed portion of the gate bus line does not have the non-linear structure. The portion of the recess overlapping the data bus line has a straight linear structure. Accordingly, Applicant respectfully submits that claims 19 and 20 are allowable over Kim.

Applicants believe the foregoing remarks place the application in condition for allowance and early, favorable action is respectfully solicited.

If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at (202) 496-7500 to discuss the steps necessary for placing the application in condition for allowance. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. § 1.136, and any additional fees required under 37

C.F.R. § 1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

Dated: January 18, 2006

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